

Figure 1

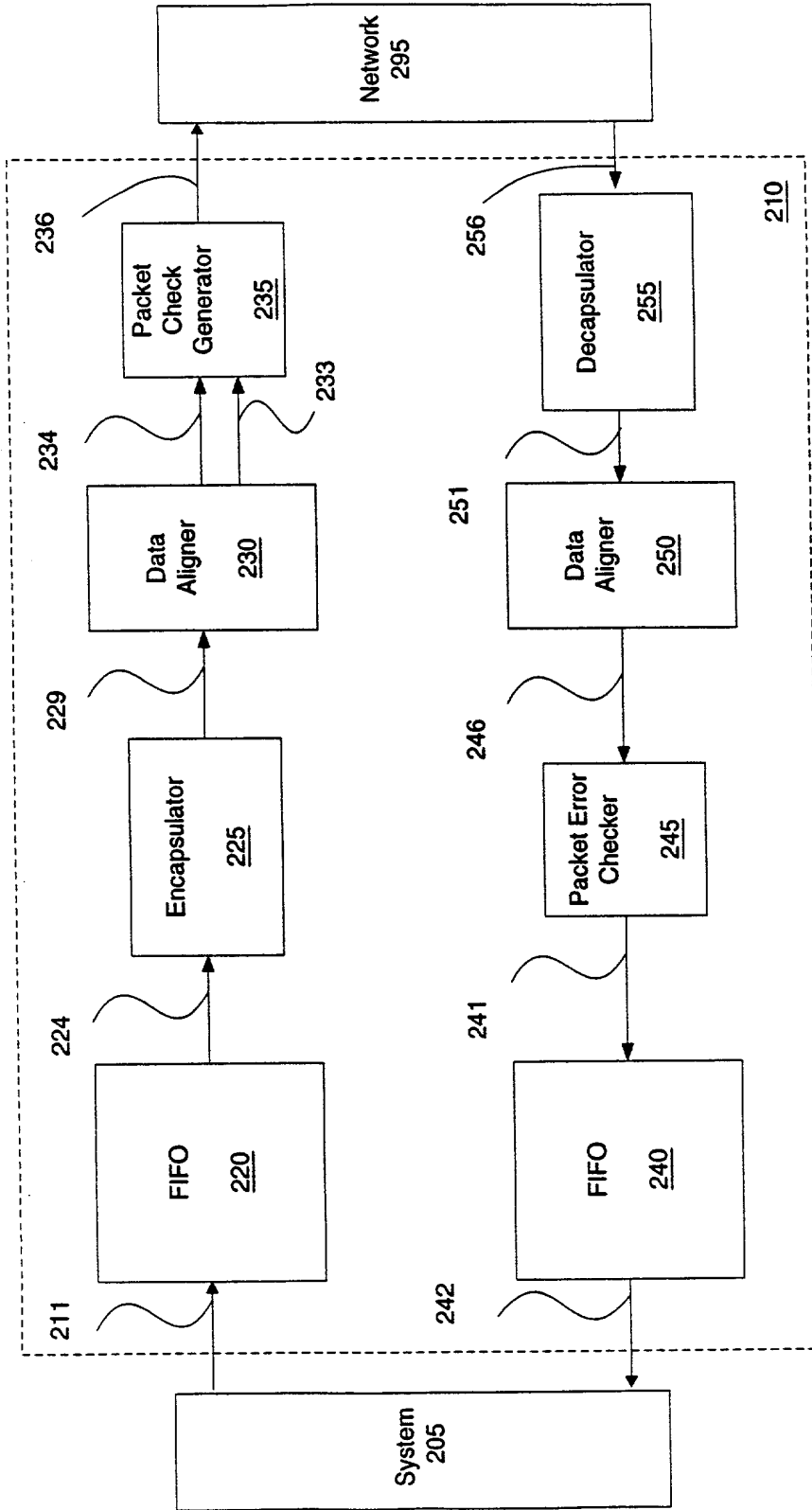


Figure 2

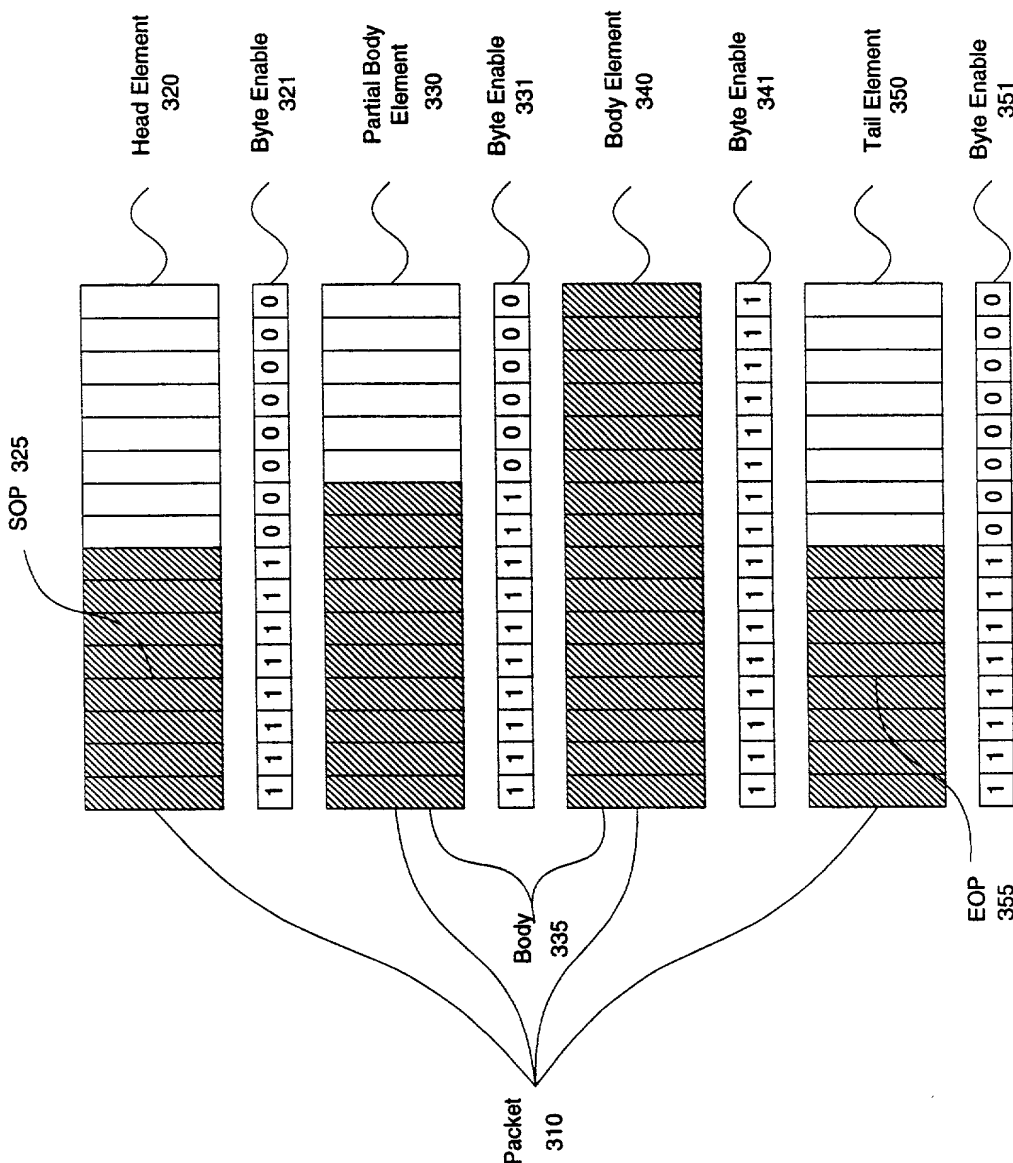


Figure 3

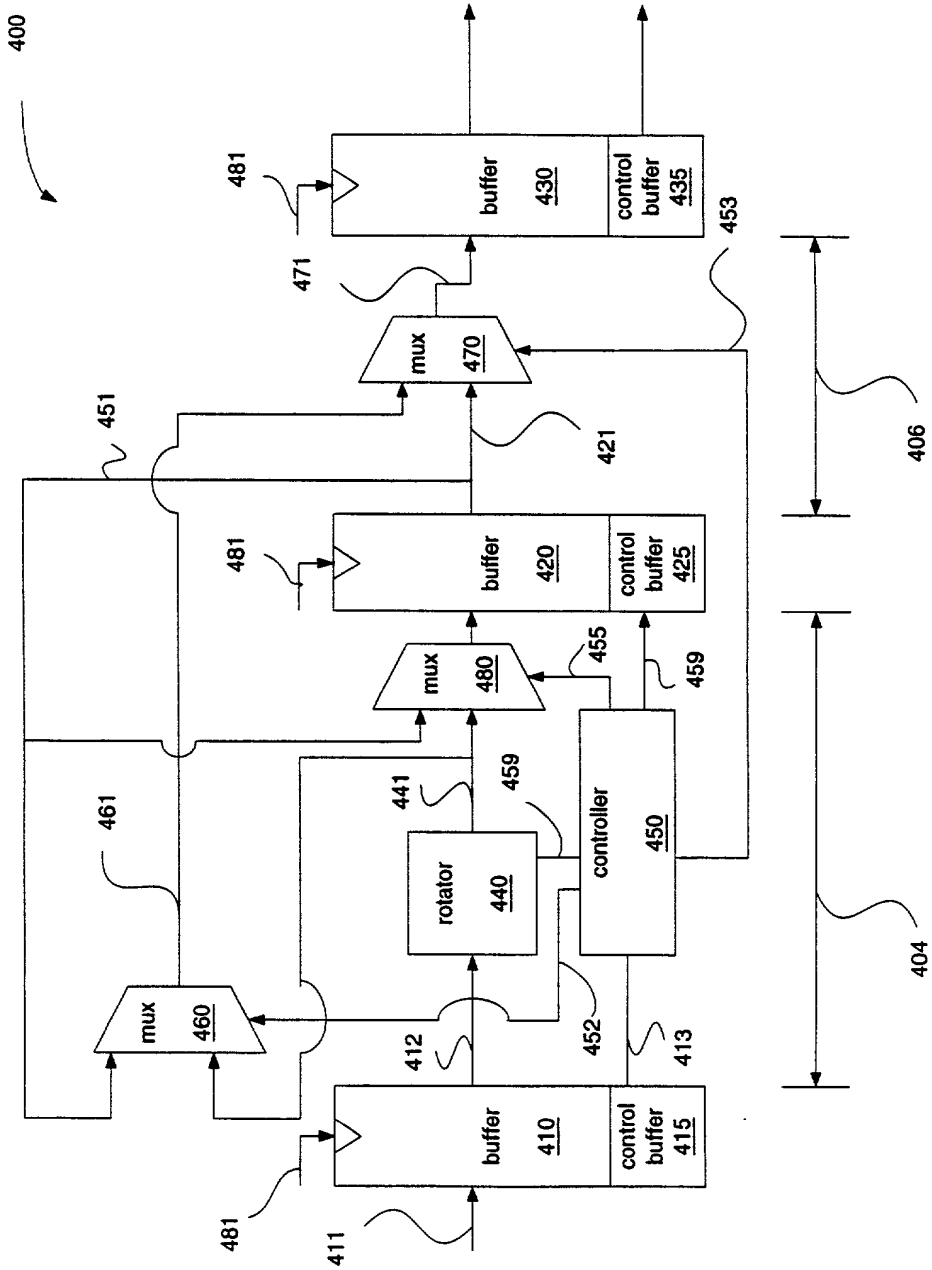


Figure 4

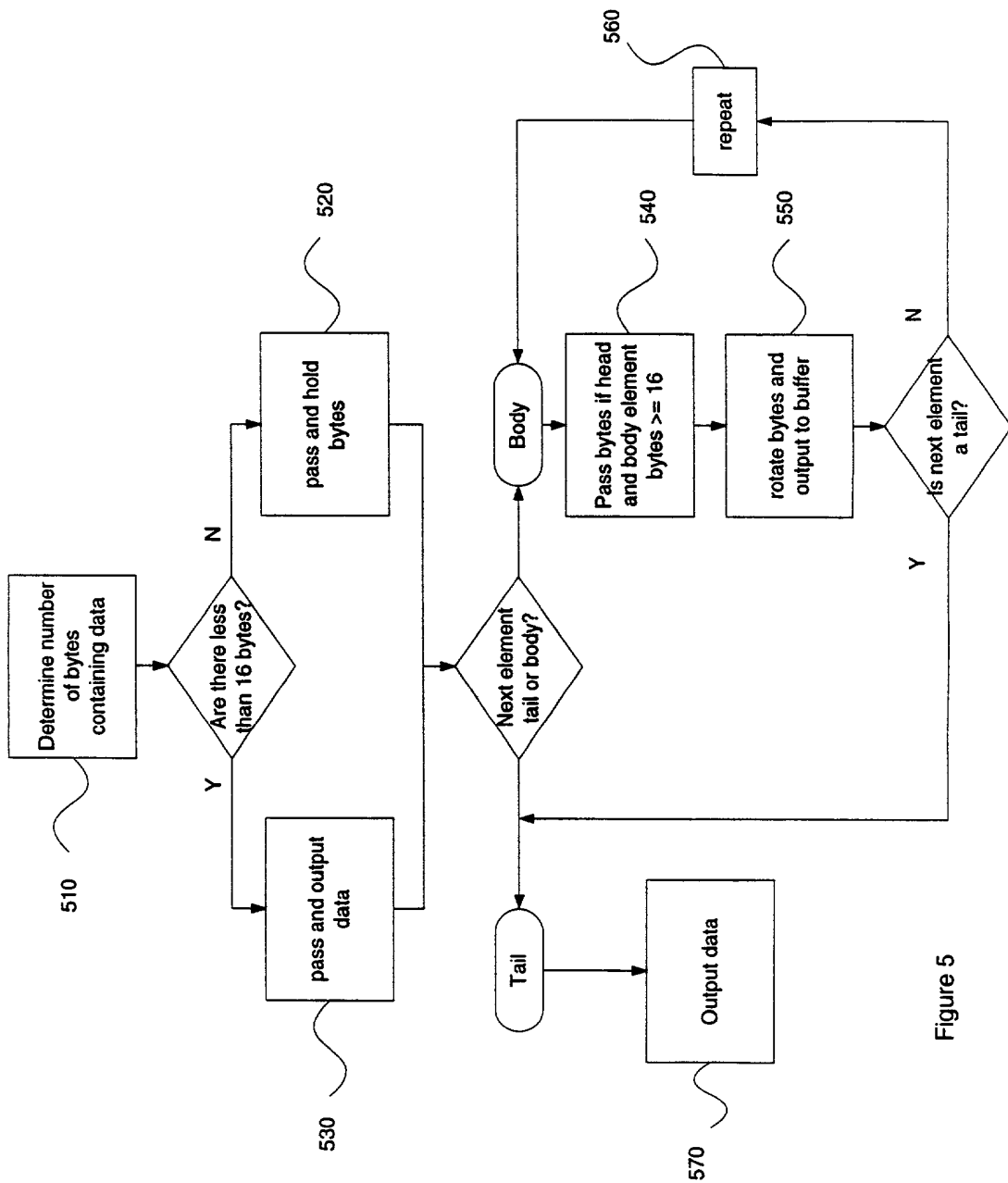


Figure 5

Complex Data stream Element	Mapped Data stream Element	Qualifier	Mapping Flow
Head	Head		Same sequence as in simple data stream
Body	Body		Same sequence as in simple data stream
Tail	Tail		Same sequence as in simple data stream
Hole	Hold		Hold state
Partial Body (Tail A)	Tail	Net Count <16	Follow tail sequence but: Suppress data aligner control output. Bypass intermediate buffer. Perform calculation using unpassed result.
		Net Count >=16	Follow tail sequence but: Do not suppress byte enables, SOP. Suppress generation of EOP control signal. No bypass for computation.

Figure 6

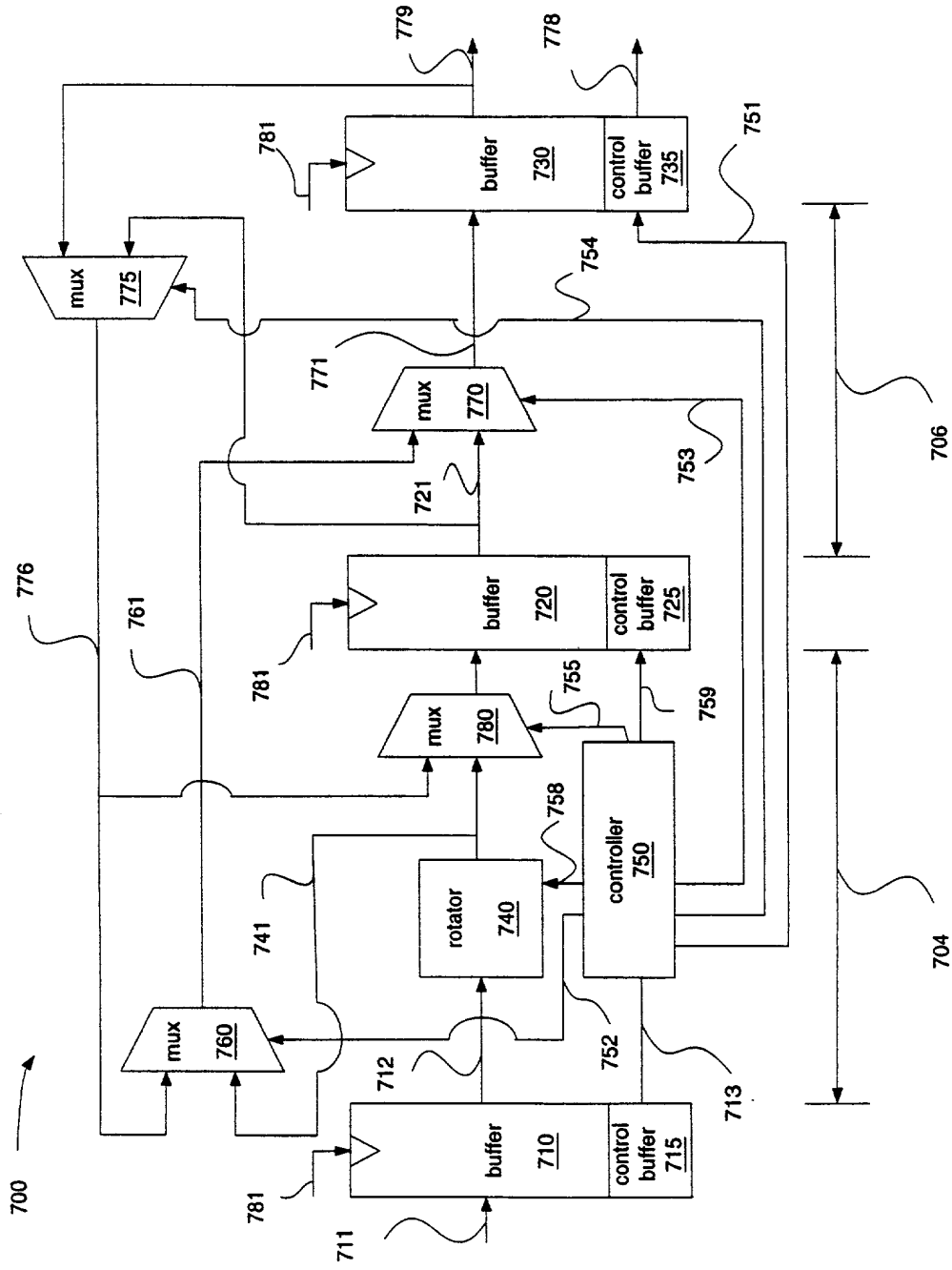
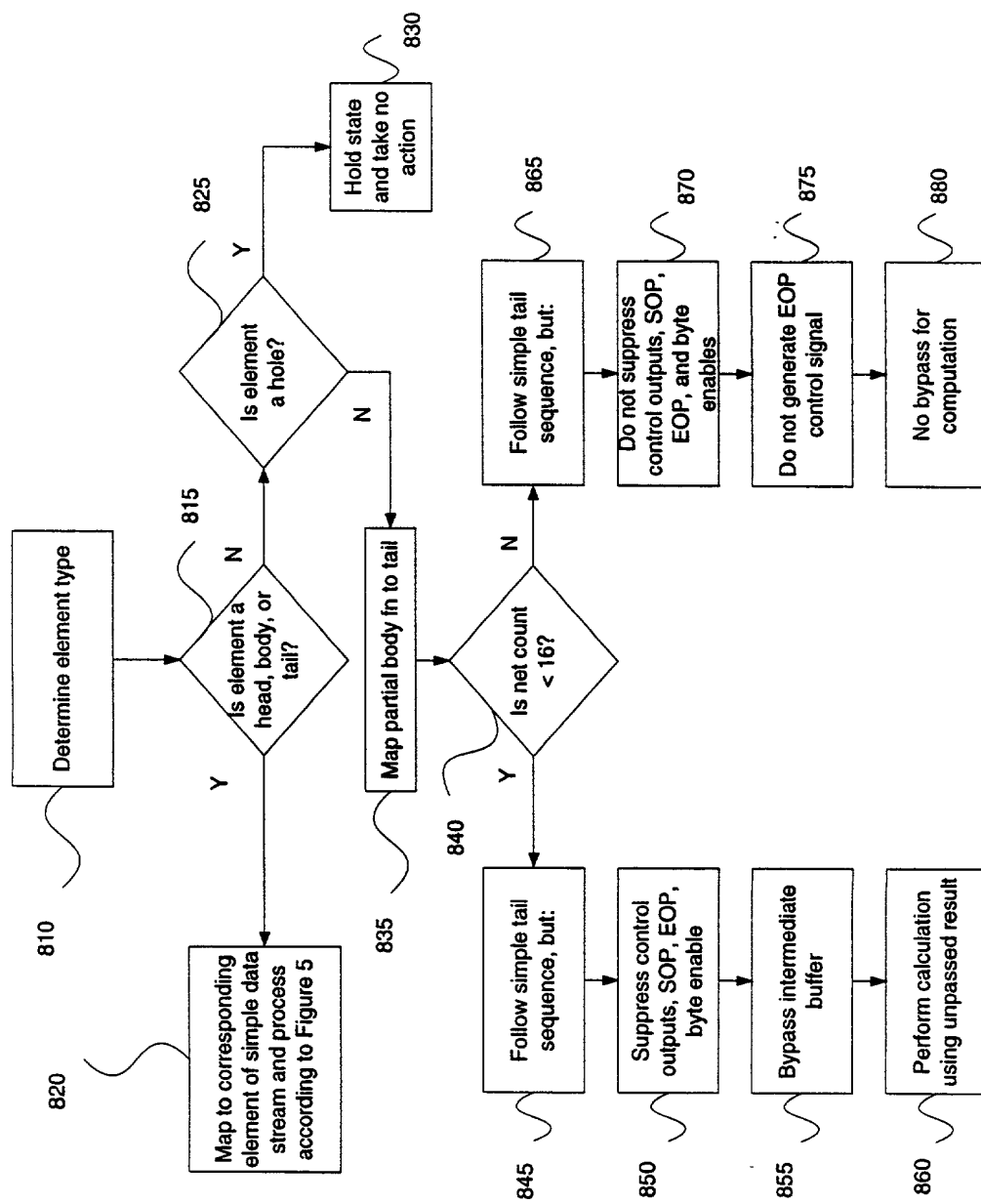


Figure 7



900

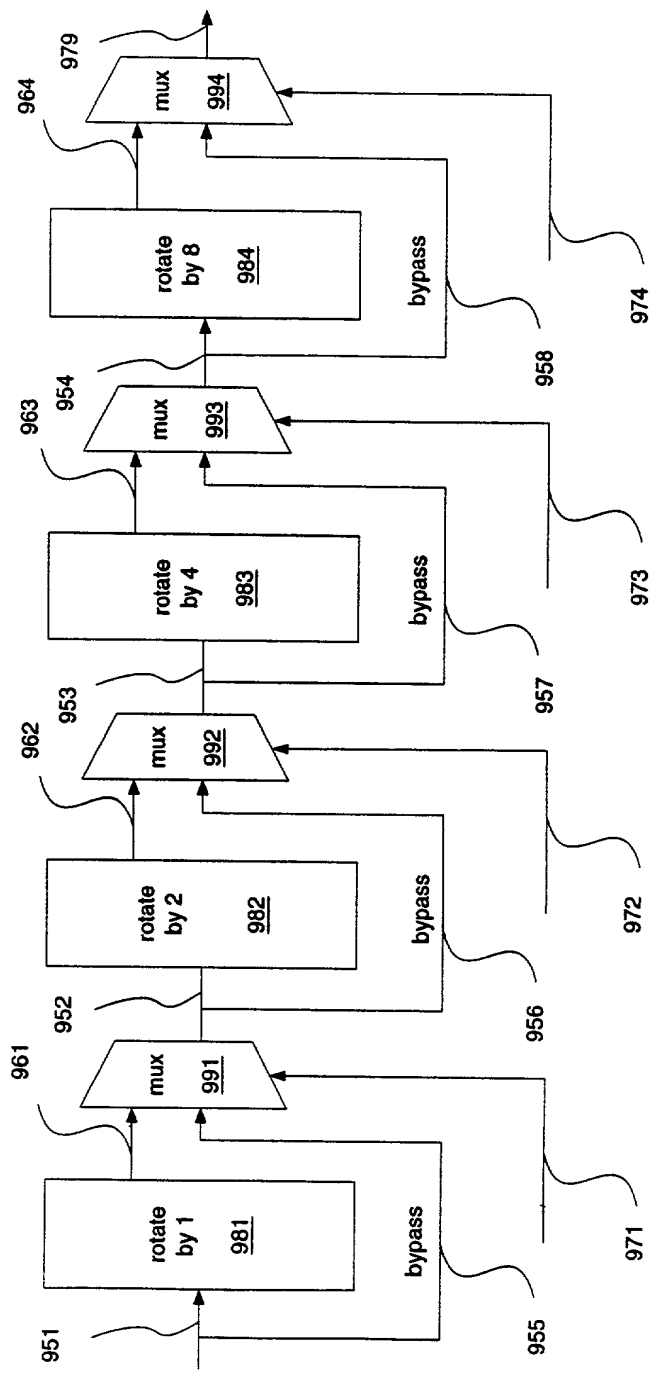


Figure 9

Serial Number	Shift Amount Value	Value of the Mux Control signal mxcntl[15:0]
1	4'b0000	16'b0000000000000000
2	4'b0001	16'b1000000000000000
3	4'b0010	16'b1100000000000000
4	4'b0011	16'b1110000000000000
5	4'b0100	16'b1111000000000000
6	4'b0101	16'b1111100000000000
7	4'b0110	16'b1111110000000000
8	4'b0111	16'b1111111000000000
9	4'b1000	16'b1111111100000000
10	4'b1001	16'b1111111110000000
11	4'b1010	16'b1111111111000000
12	4'b1011	16'b1111111111100000
13	4'b1100	16'b1111111111110000
14	4'b1101	16'b1111111111111000
15	4'b1110	16'b1111111111111100
16	4'b1111	16'b1111111111111110

FIGURE 10

Rotate Amount	Input	Output
0	{ABCDEFGHIJKLMN}	{ABCDEFGHIJKLMN}
1	{ABCDEFGHIJKLMN}	{PABCDEFGHIJKLM}
2	{ABCDEFGHIJKLMN}	{OPABCDEFGHIJLM}
3	{ABCDEFGHIJKLMN}	{NOPABCDEFGHIJLM}
4	{ABCDEFGHIJKLMN}	{MNOPABCDEFGHIJL}
5	{ABCDEFGHIJKLMN}	{LMNOPABCDEFGHIJK}
6	{ABCDEFGHIJKLMN}	{KLMNOPABCDEFGHIJ}
7	{ABCDEFGHIJKLMN}	{JKLMNOPABCDEFGHI}
8	{ABCDEFGHIJKLMN}	{IJKLMNOPABCDEFGHI}
9	{ABCDEFGHIJKLMN}	{HIJKLMNOPABCDEFG}
10	{ABCDEFGHIJKLMN}	{GHIJKLMNOPABCDEF}
11	{ABCDEFGHIJKLMN}	{FGHIJKLMNOPABCDE}
12	{ABCDEFGHIJKLMN}	{EFGHIJKLMNOPABCD}
13	{ABCDEFGHIJKLMN}	{DEFGHIJKLMNOPABC}
14	{ABCDEFGHIJKLMN}	{CDEFGHIJKLMNOPAB}
15	{ABCDEFGHIJKLMN}	{BCDEFGHIJKLMNOPA}

FIGURE 17